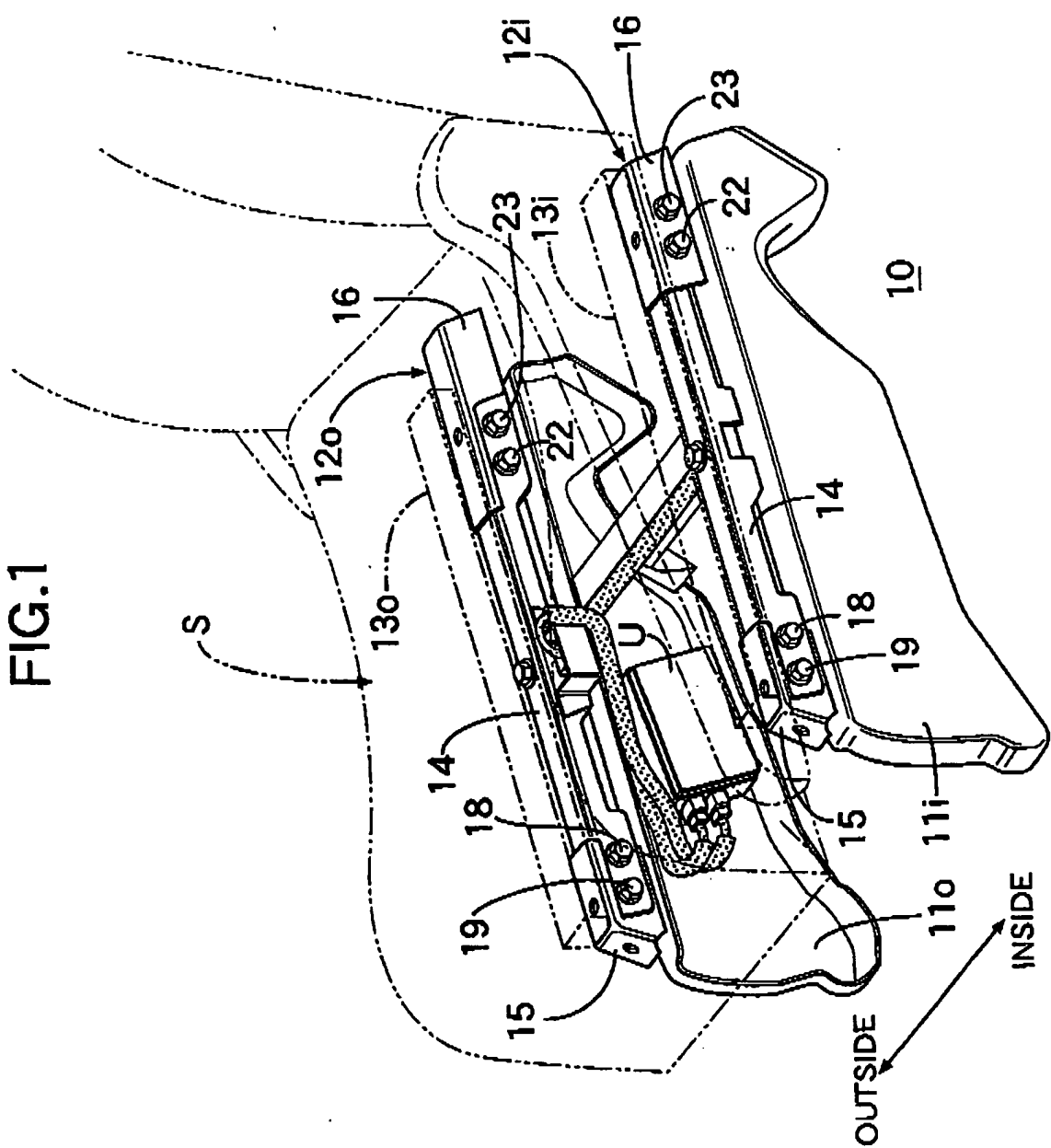


FIG.1



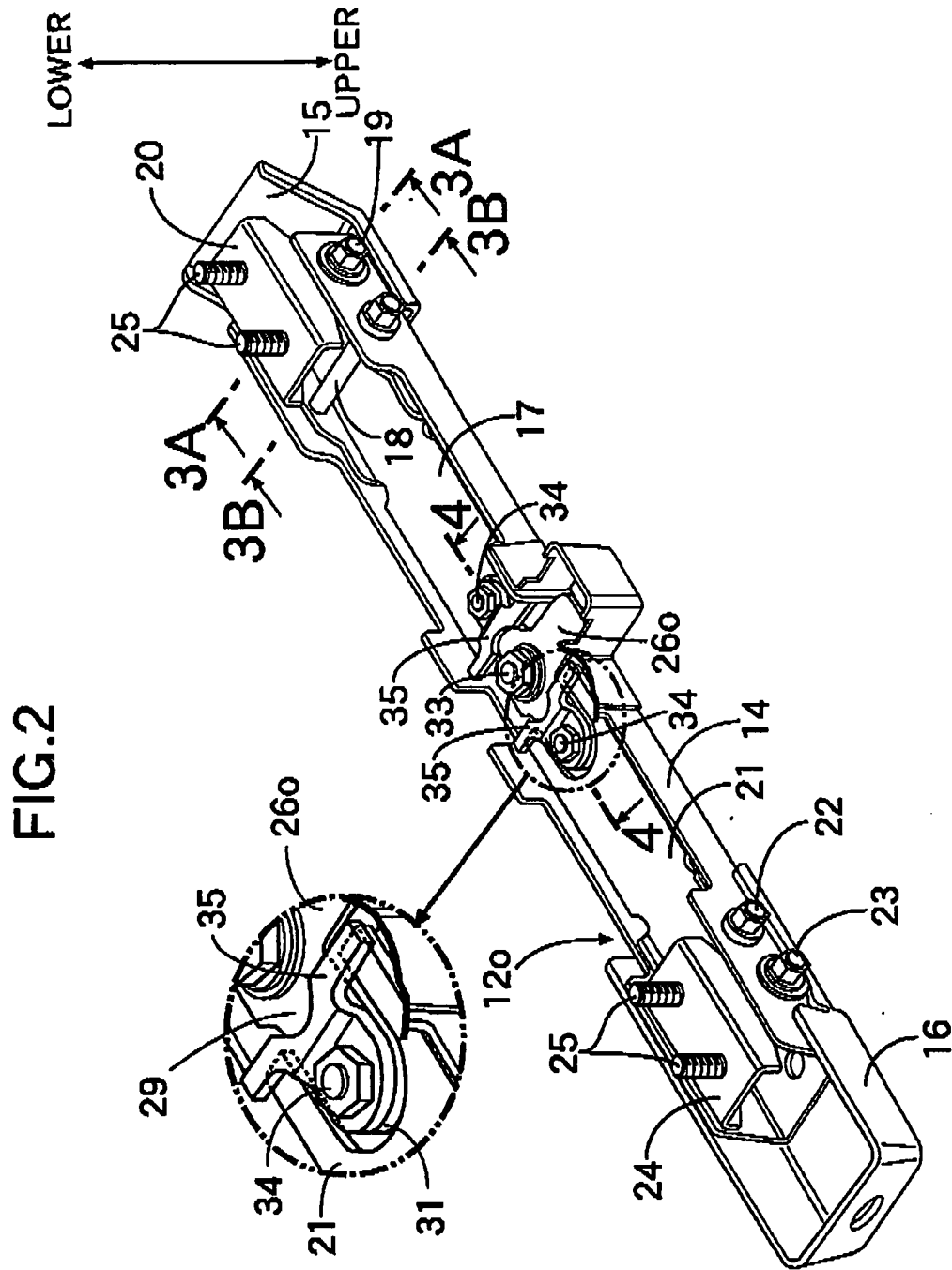


FIG.3A

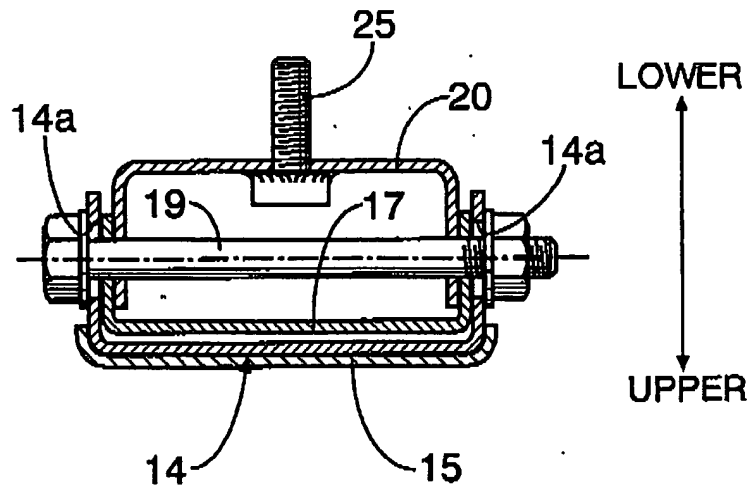


FIG.3B

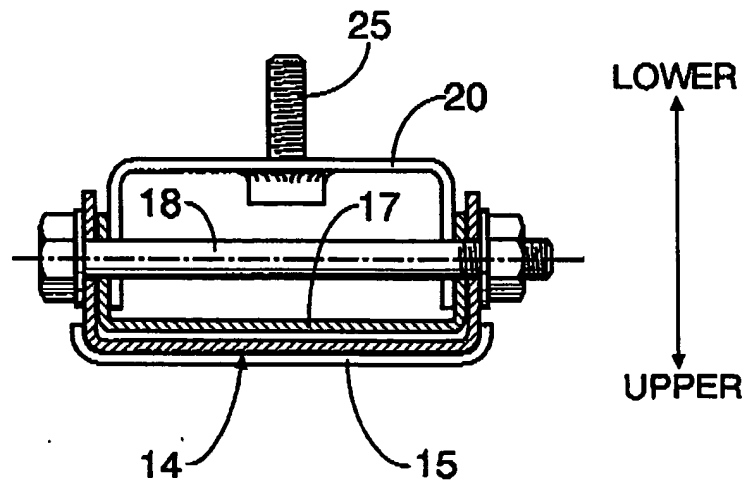


FIG.4

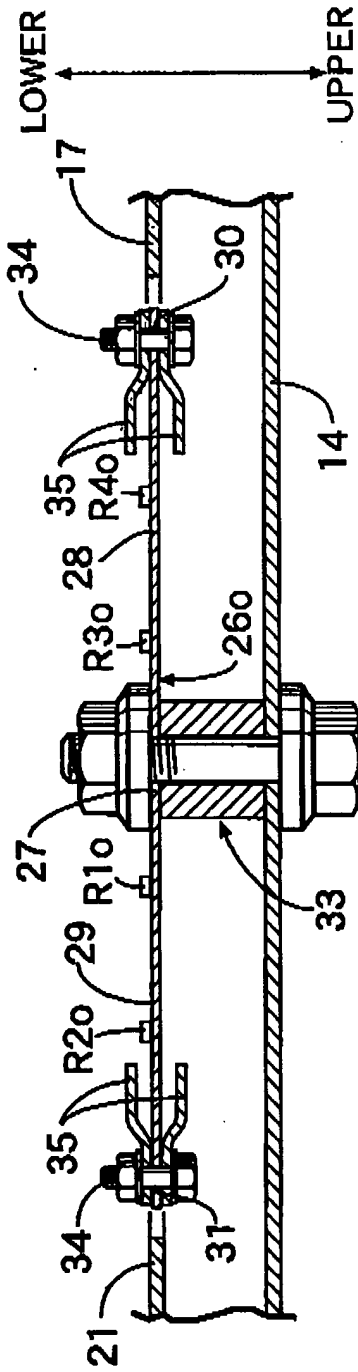


FIG.5

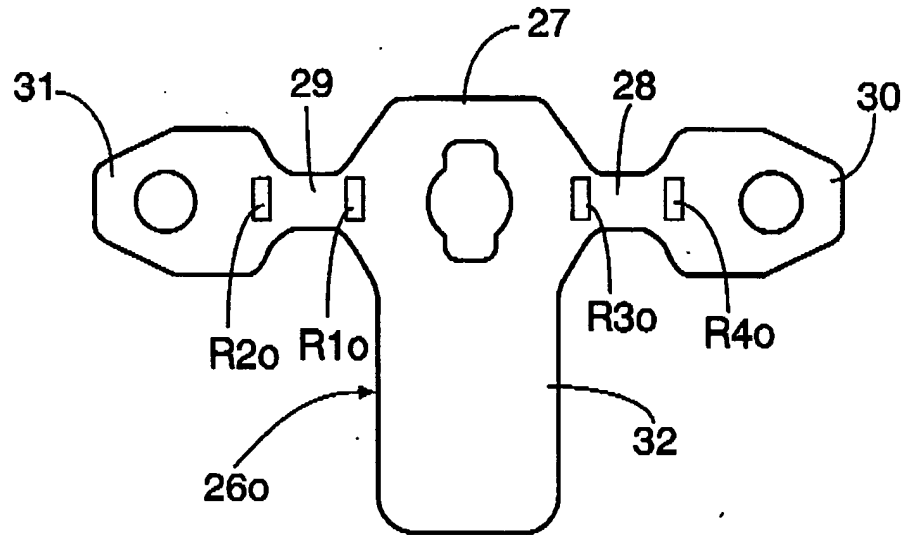


FIG. 6

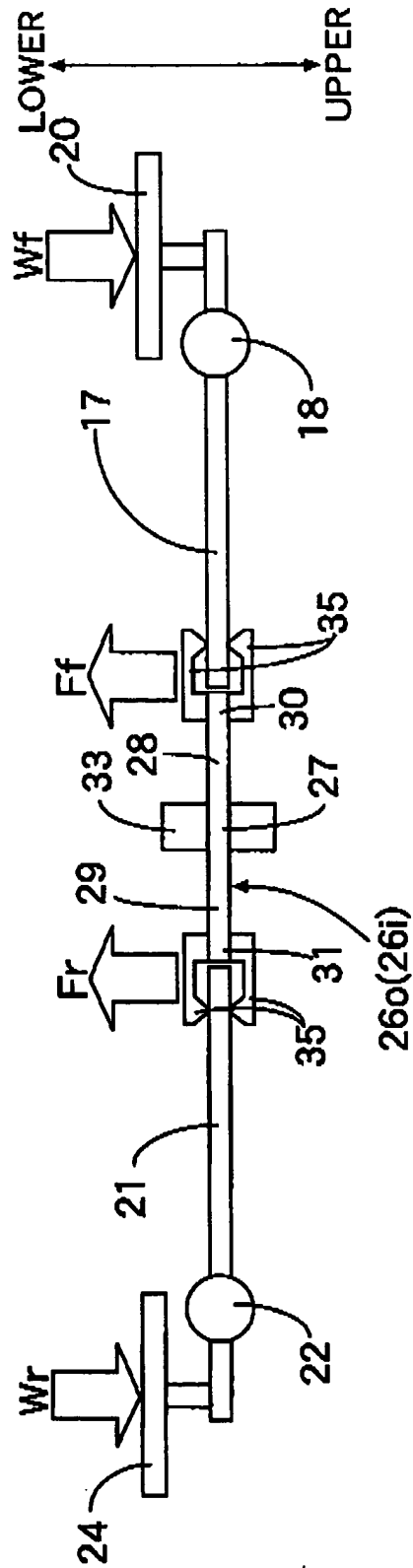


FIG.7A

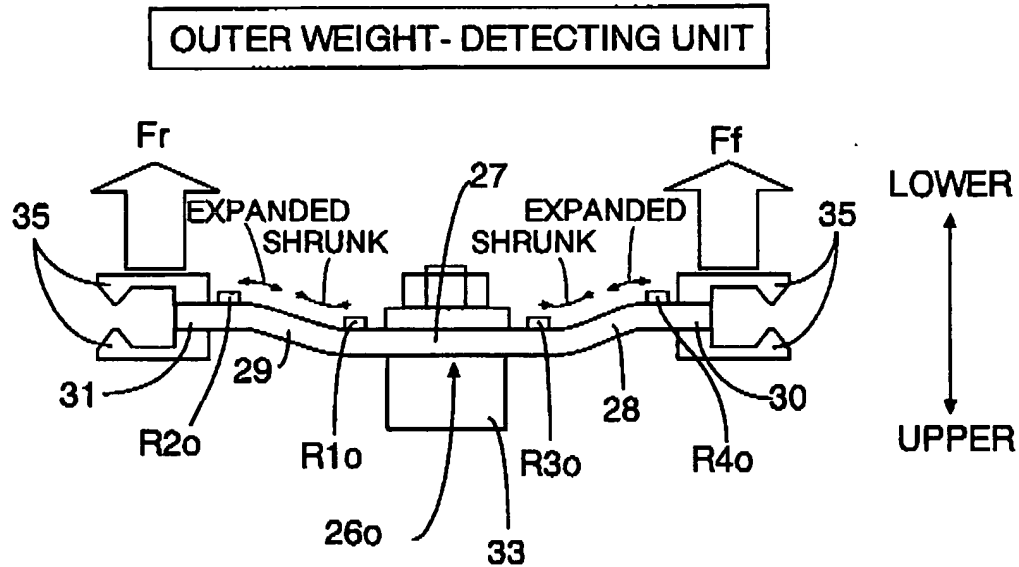


FIG.7B

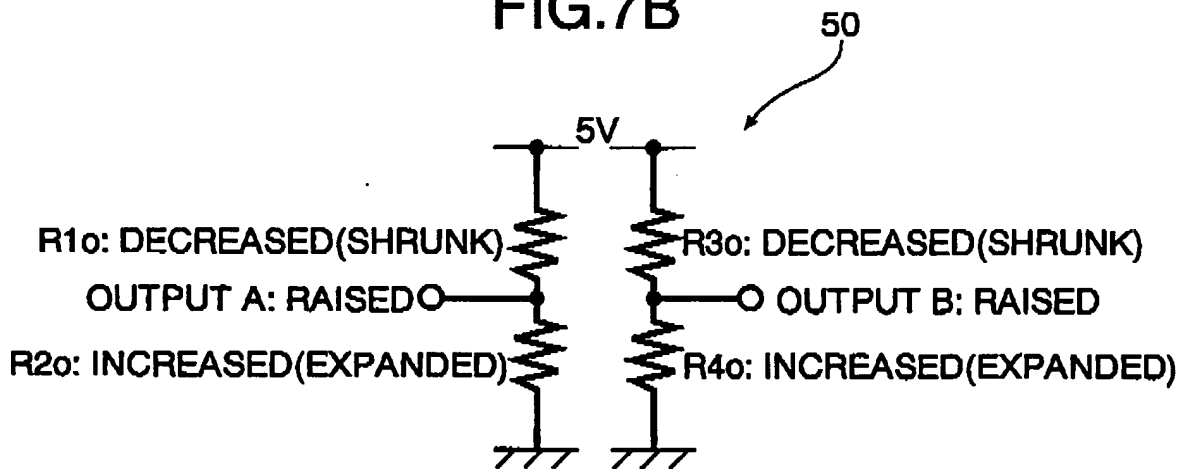


FIG.8A

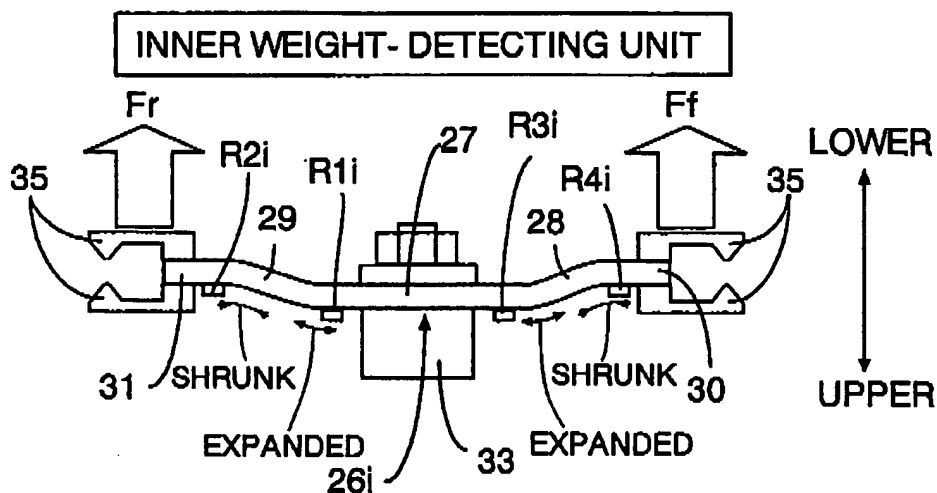


FIG.8B

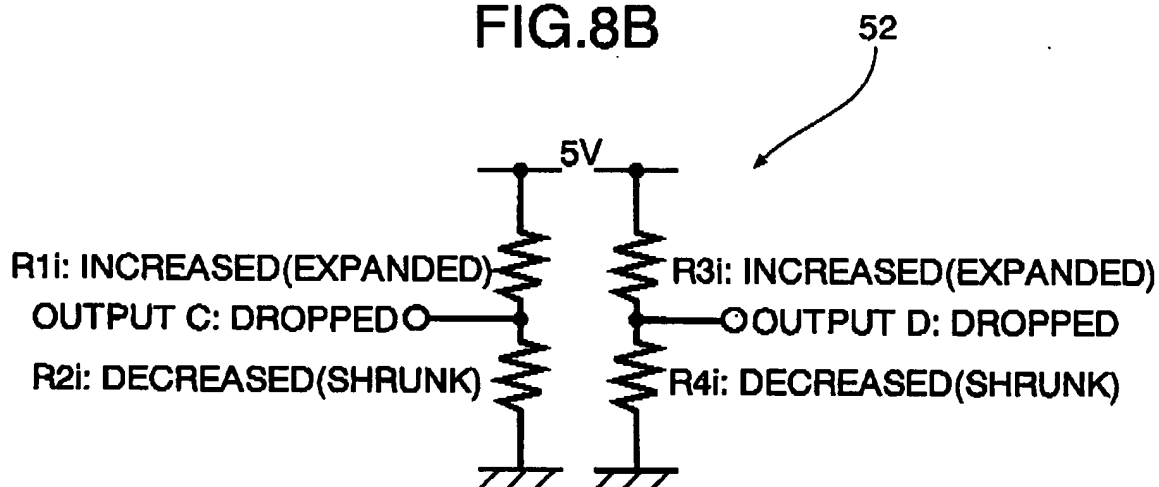


FIG.9

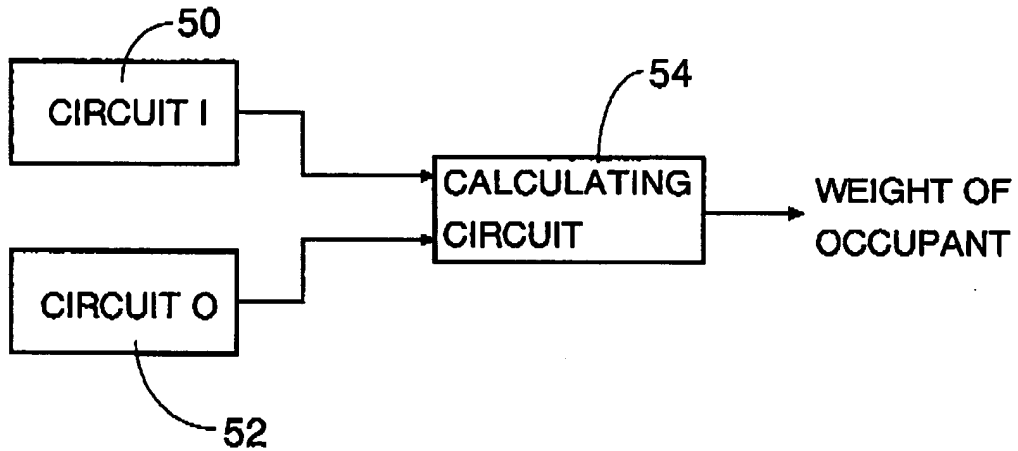


FIG.10A

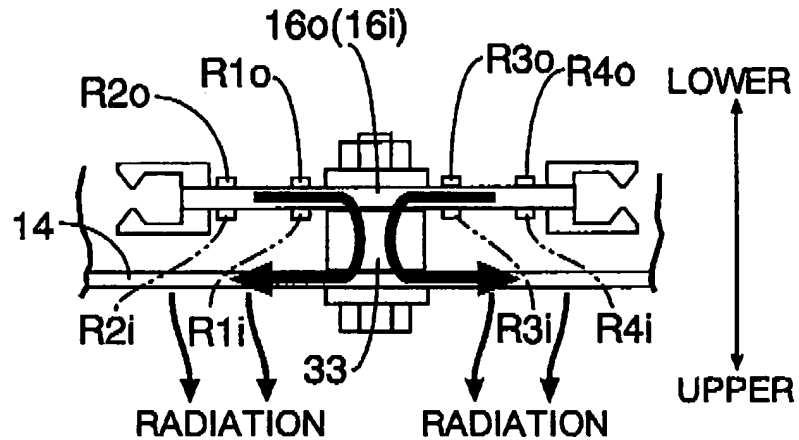


FIG.10B

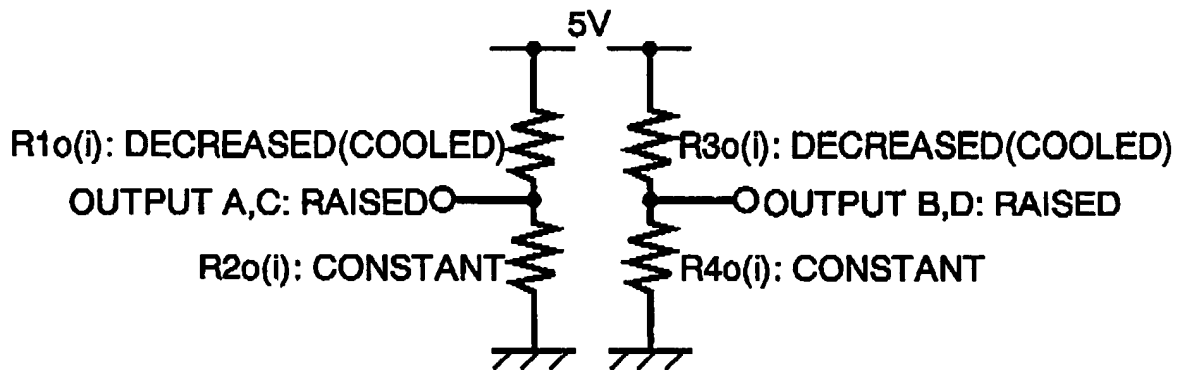


FIG.11

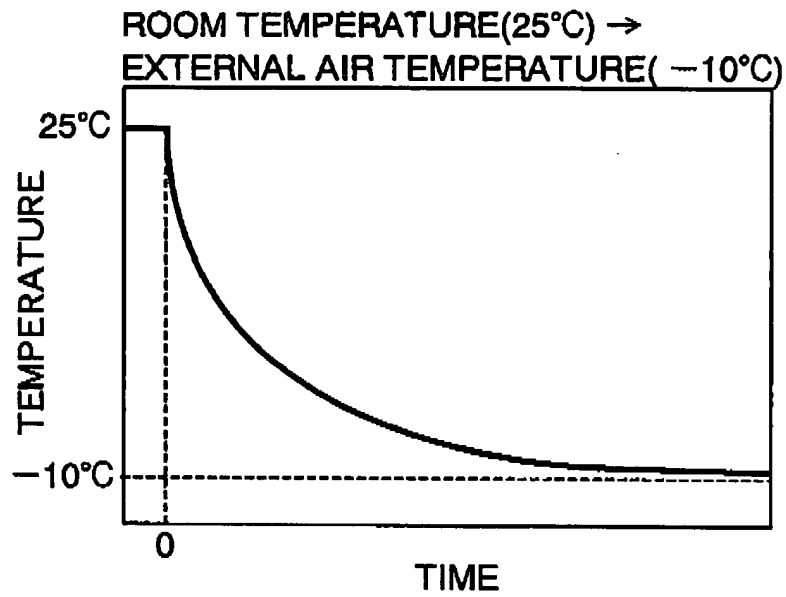


FIG.12

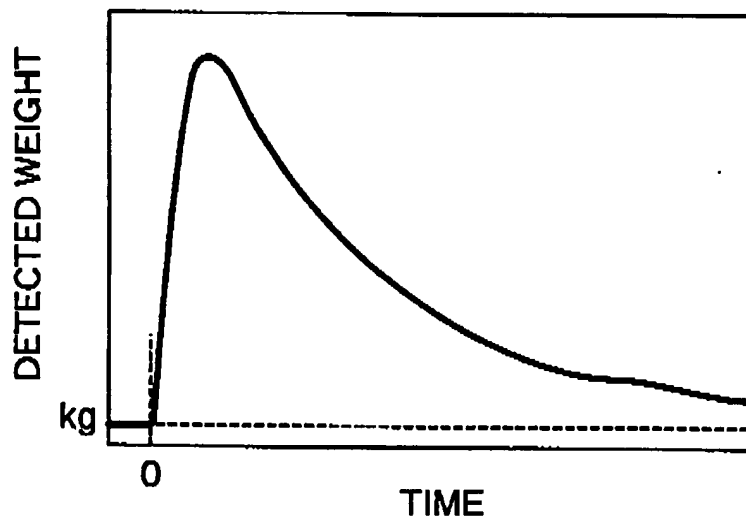


FIG13A

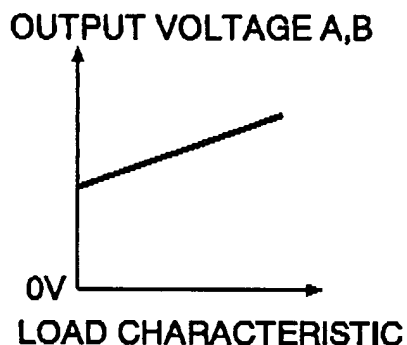


FIG13B

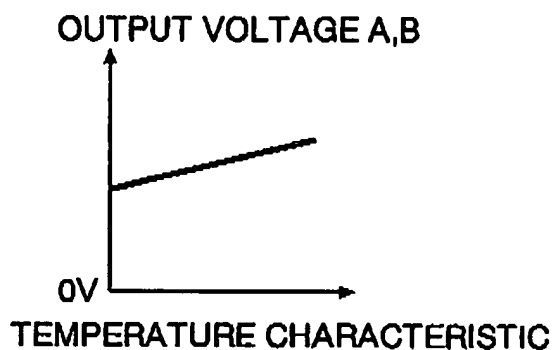


FIG14A

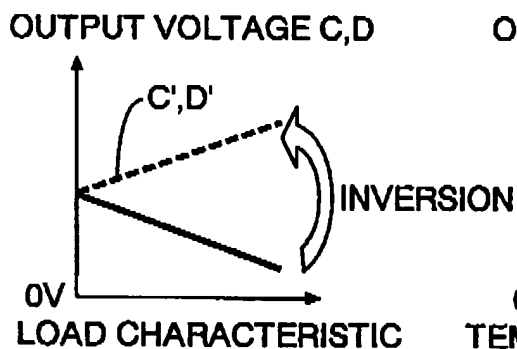


FIG14B

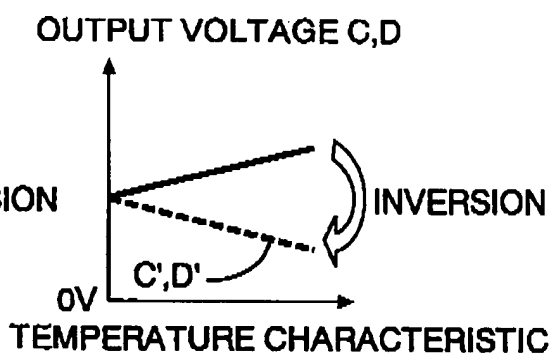


FIG15A

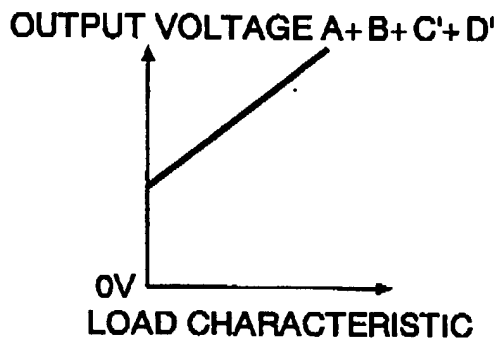
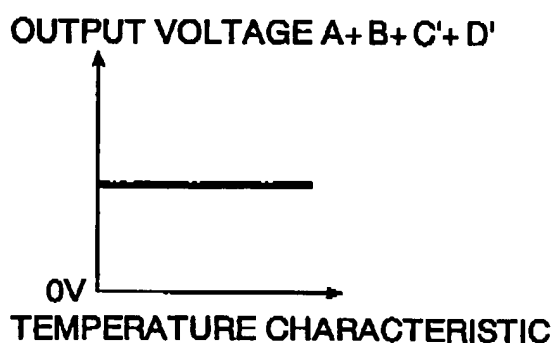


FIG15B



LOAD CHARACTERISTICS ARE
EQUIVALENT TO THOSE WHEN
STRAIN RESISTORS ARE
MOUNTED ON LOWER SURFACE

TEMPERATURE CHARACTERISTIC
ARE COUNTERVAILED